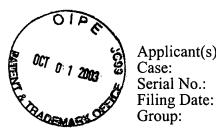
## FORM PTO-1449 (MODIFIED)

## LIST OF PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT



Applicant(s): Jonathan H. Fischer

Case: Serial No.:

10/002,028

November 15, 2001 2828

**U.S. PATENT DOCUMENTS** 

EXAMINER					FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS** 

TRANSLATION **EXAMINER** COUNTRY YES INITIAL DOCUMENT NO.

**OTHER DOCUMENTS** 

EXAMINER

AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

1. P.R. Gray et al., "Analysis and Design of Analog Integrated Circuits," Third Edition, John Wiley & Sons, Inc., pp. 227-228, 1993.

-2. Alan B. Grebene, "Bipolar and MOS Analog Integrated Circuit Design," John Wiley & Sons, Inc., pp. 215-216 and 278-280, 1984.

TECHNOLOGY CENTER 2800

Examiner	Date Considered
EXAMINE	

with Ge-10/29/03

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.